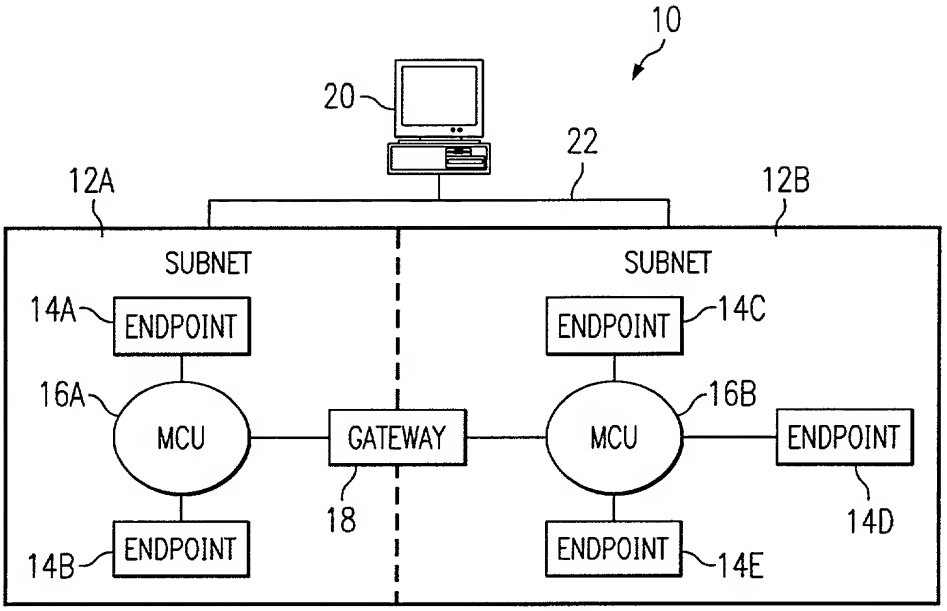

10
20
22
12A
12B
SUBNET
SUBNET
14A
ENDPOINT
ENDPOINT
14C
16A
MCU
GATEWAY
MCU
16B
ENDPOINT
18
14D
14B
ENDPOINT
ENDPOINT
14E

FIG. 1

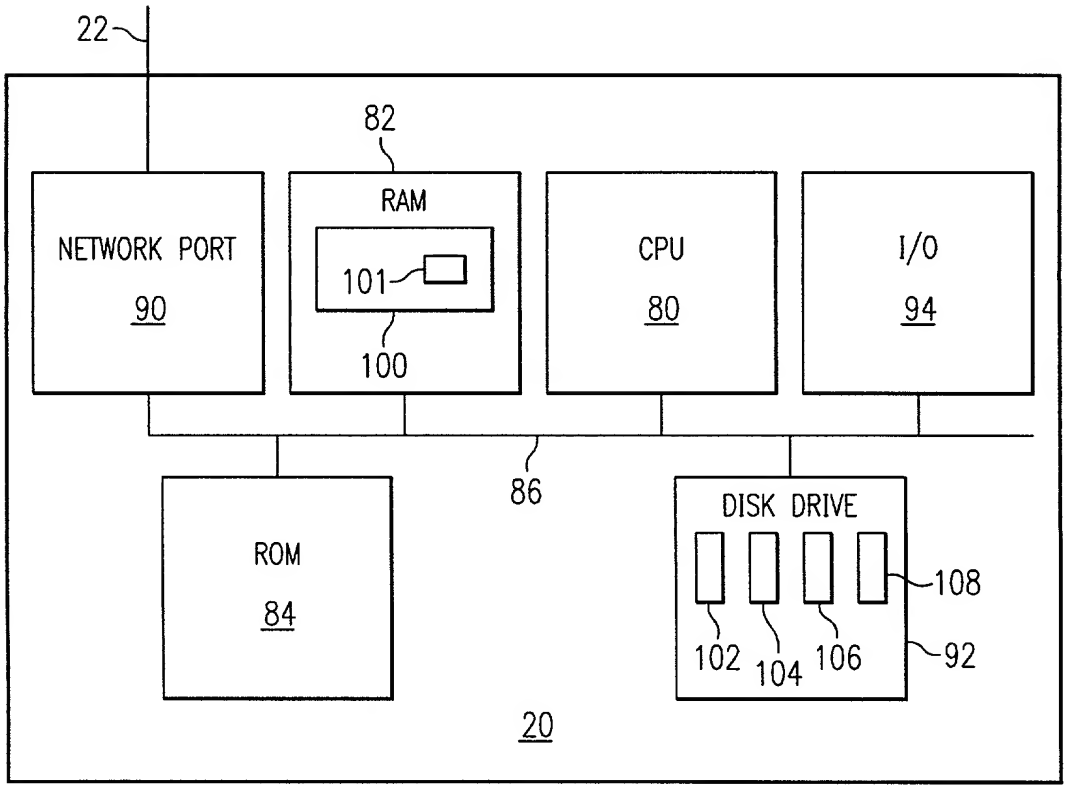
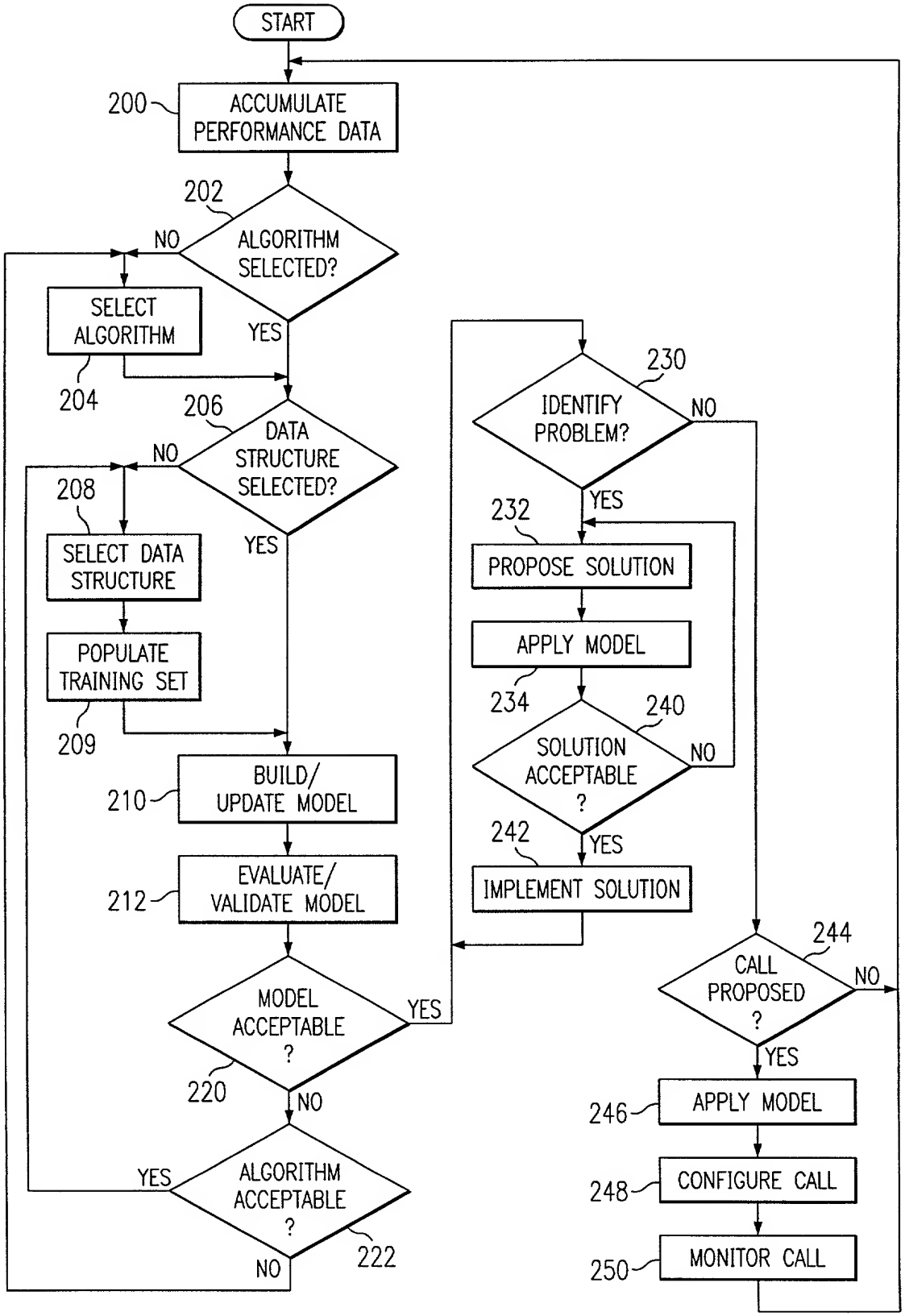

22
82
NETWORK PORT
90
RAM
101
100
CPU
80
I/O
94
86
ROM
84
DISK DRIVE
108
102
104
106
92
20

FIG. 6

FIG. 2


START
200
ACCUMULATE PERFORMANCE DATA
202
ALGORITHM SELECTED?
NO
YES
SELECT ALGORITHM
204
206
DATA STRUCTURE SELECTED?
NO
YES
208
SELECT DATA STRUCTURE
POPULATE TRAINING SET
209
210
BUILD/ UPDATE MODEL
212
EVALUATE/ VALIDATE MODEL
MODEL ACCEPTABLE ?
YES
220
NO
ALGORITHM ACCEPTABLE ?
YES
NO
222
230
IDENTIFY PROBLEM?
NO
YES
232
PROPOSE SOLUTION
APPLY MODEL
234
240
SOLUTION ACCEPTABLE ?
NO
242
YES
IMPLEMENT SOLUTION
244
CALL PROPOSED ?
NO
YES
246
APPLY MODEL
248
CONFIGURE CALL
250
MONITOR CALL

CALL HISTORY TABLE

CALL-ID	FROM-SUBNET	FROM-ENDPOINT	FROM-VENDOR	FROM-MODEL	GATEWAY	TO-SUBNET	TO-ENDPOINT
C1	12A	14A	VTEL	GALAXY	18	12B	14C
C2	12A	14A	VTEL	GALAXY	18	12B	14C
C3	12B	14C	ILLUDIUM	Q-36	18	12A	14A
C4	40	50	VTEL	GALAXY	52	42	54
C5	12B	14C	ILLUDIUM	Q-36	18	12A	14A
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮

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TO-VENDOR	TO-MODEL	MCU	LINE-SPEED	START-DATE	START-DAY	START-HOUR	DURATION	OUTCOME
ILLUDIUM	Q-36	TRUE	256Kbps	2001-10-12	FRIDAY	10	0:01:45	2
ILLUDIUM	Q-36	TRUE	512Kbps	2001-10-13	SATURDAY	10	0:20:00	12
VTEL	GALAXY	TRUE	256Kbps	2001-10-16	TUESDAY	12	2:00:00	0
ILLUDIUM	Q-36	TRUE	256Kbps	2001-10-16	TUESDAY	16	1:10:00	0
VTEL	GALAXY	FALSE	512Kbps	2001-10-19	FRIDAY	9	0:01:50	2
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮

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FIG. 3

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RULE 1:	TO-MODEL=Q-36	&	MCU=TRUE	→	CLASS/=0 [70.7%]
RULE 2:	FROM-VENDOR=VTEL	&	TO-VENDOR=ILLUDIUM	→	CLASS/=0 [63.0%]

FIG. 4

PROPOSED CALL

FROM-SUBNET	FROM-ENDPOINT	FROM-VENDOR	FROM-MODEL	GATEWAY	TO-SUBNET	TO-ENDPOINT
12A	14A	VTEL	GALAXY	18	12B	14C

TO-VENDOR	TO-MODEL	MCU	LINE-SPEED	START-DATE	START-DAY	START-HOUR	DURATION
POLYCOM	VIA VIDEO	FALSE	256Kbps	2001-10-22	MONDAY	10	2:00:00

FIG. 5